ABSTRACT OF THE DISCLOSURE

A liquid crystal display device includes a plurality of gate lines and data lines arranged horizontally and vertically, respectively, for defining a plurality of pixel areas; a plurality of switching devices formed at intersections of the gate lines and the data lines; and a pixel electrode formed in a pixel area connected to the switching device corresponding to the pixel area and partially overlapping the data lines adjacent to the corresponding pixel area, wherein a first parasitic capacitance generated by the pixel electrode overlapping a data line for the corresponding pixel area and a second parasitic capacitance generated the pixel electrode overlapping a data line for an adjacent pixel area are substantially equal to each other.